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	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<u>L16</u>	L5 and L15	3	<u>L16</u>
<u>L15</u>	(K1 or K2 or L14) and L13	7	<u>L15</u>
<u>L14</u>	LP-S or ((lockout or lock-out) near1 protect\$6)	30905	<u>L14</u>
<u>L13</u>	L1 and L12	10	<u>L13</u>
<u>L12</u>	(T1 or T2 or (at near1 time\$)) and L11	27	<u>L12</u>
<u>L11</u>	6993684.uref. or 6349092.uref. or 5550805.uref. or 6256292.uref. or 6122250.uref. or 6456587.pn. or 6456587.uref.	93	<u>L11</u>
<u>L10</u>	6993684.pn. or 6349092.pn.	2	<u>L10</u>
<u>L9</u>	(at near1 time\$) and L6	5	<u>L9</u>
<u>L8</u>	time\$ and L6	10	<u>L8</u>
<u>L7</u>	(K1 or K2) and L6	7	<u>L7</u>
<u>L6</u>	L1 same L5	10	<u>L6</u>
<u>L5</u>	(pass-through or (pass near1 through)) near1 state\$	1768	<u>L5</u>

<u>L4</u>	((K near1 byte\$) or K-byte\$) near1 (pass-through or (pass near1 through)) near1 state\$	1	<u>L4</u>
<u>L3</u>	(full\$6 near1 (pass-through or (pass near1 through)) near1 state\$)	15	<u>L3</u>
<u>L2</u>	(K near1 (pass-through or (pass near1 through)) near1 state\$)	1	<u>L2</u>
<u>L1</u>	((bidirectional or bi-directional) near1 (line-switch\$6 or (line near1 switch\$6)) near1 ring\$) or BLSR\$	567	<u>L1</u>

END OF SEARCH HISTORY

Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 09812419 **Filing Dt:** 03/20/2001**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** US20020039348**Pub Dt:** 04/04/2002**Inventors:** Takashi Honda, Yusuke Ishii**Title:** Method for controlling switching in bidirectional line-switched ring**Assignment: 1****Reel/Frame:** 011631 /
0236**Received:**
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06/05/2001**Pages:**
3**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** HONDA, TAKASHI**Exec Dt:** 03/12/2001ISHII, YUSUKE**Exec Dt:** 03/12/2001**Assignee:** FUJITSU LIMITED

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